### Tulsiramji Gaikwad Patil College of Engineering & Technology, Nagpur

#### SCHEME OF INSTRUCTION

Programme: Electronics & Communication Engineering

Scheme of Instructions: Final Year B. Tech. in Electronics & Communication Engineering

Semester-VII

Sr.	Course		C TEM	_	Œ	ъ	Contact	G 114		E	XAM SCH	IEME	
No.	Category	CourseCode	Course Title	L	1	P	Hrs./Wk	Credits	CT1	CT2	TA/CA	ESE	TOTAL
1	PCC	BEC4701	Computer Communication Network	3	-	-	3	3	15	15	10	60	100
2	PCC	BEC4702	CMOS VLSI Design	3	1	-	4	4	15	15	10	60	100
3	PEC	BEC4703-5	Program ElectiveV	3	-	-	3	3	15	15	10	60	100
4	OEC	B\$\$XX01-16	Open Elective-III	3	-	-	3	3	15	15	10	60	100
5	OEC	B\$\$XX01-16	Open Elective-IV	3	-	-	3	3	15	15	10	60	100
6	OEC	B\$\$XX01-16	Open Elective-V	3	-	-	3	3	15	15	10	60	100
7	PCC	BEC4706	Computer Communication Network Lab	-	-	2	2	1	-	-	25	25	50
8	PCC	BEC4707	CMOS VLSI Design Lab	-	-	2	2	1	-	-	25	25	50
9	PROJ	BEC4708	Seminar	-	-	2	2	1	-	-	25	25	50
10	MCC	AU4710	Innovations and Society	2	-	-	2	Audit	-	-	-	-	-
			Total	20	1	6	27	22	90	90	135	435	750

<sup>\*</sup>There will be two presentations, based on seminar topic to be selected in consultation with guide preferably based on emerging trends.

L- Lecture T-Tutorial P-Practical

CT1- Class Test 1 TA/CA- Teacher Assessment/Continuous Assessment

CT2- Class Test 2 ESE- End Semester Examination (For Laboratory End Semester performance)

Course Category	HSMC (Hum.,	BSC	ESC	PCC (Programme	PEC (Programme	OEC (Open	Project / Seminar	MCC (Mandatory
	Soc. Sc, Mgmt.)	(Basic Sc.)	(E+ngg.	Core courses)	Elective courses)	Elective courses	/ Industrial	Courses)
			Sc.)			from other	Training	
						discipline)		
Credits	-	1	1	9	03	09	01	Yes
Cumulative Sum	05	24	24	47	12	06	04	

PROGRESSIVE TOTAL CREDITS :122++22 =144

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Tulsiramii Gaikwad-Patii College Of Engineering & Teuhnology, Nagpur

# Tulsiramji Gaikwad Patil College of Engineering & Technology, Nagpur

#### SCHEME OF INSTRUCTION

Programme: Electronics & Communication Engineering

Scheme of Instructions: Final Year B. Tech. in Electronics & Communication Engineering

Semester - VIII

Sr.	Course	Course	Course Title	L	Т	P	Contact			EXAM SCHEME			
No.	Category	Code	Course Title		1	r	Hrs./Wk	Credits	CT1	CT2	TA/CA	ESE	TOTAL
1	PROJ	BEC4801	Industry Based Project /Internship	-	-	26	26	13	1	-	75	75	150
2	PROJ	BEC4802	Comprehensive Viva-voce	-	-	1	-	4	-	-	-	100	100
3	HSMC2	BEC4803	Extra-Curricular Activities / Competitive Exam/Co-Curricular activities	-	-	4	4	2	1	-	100	-	100
4	MCC	BAU4808	Project Based Science, Technology Social Design and Innovation	2	-	-	2	Audit	-	-	-	-	-
			Total	2	-	30	32	19	-	-	175	175	350

L- Lecture T-Tutorial P-Practical

CT1- Class Test 1 TA/CA- Teacher Assessment/Continuous Assessment

CT2- Class Test 2 ESE- End Semester Examination (For Laboratory End Semester performance)

Course Category	HSMC (Hum.,	BSC	ESC	PCC (Programme	PEC (Programme	OEC (Open	Project / Seminar	MCC (Mandatory
	Soc. Sc, Mgmt.)	(Basic Sc.)	(Engg. Sc.)	Core courses)	Elective courses)	Elective courses from other	/ Industrial Training	Courses)
			20.)			discipline)	Training	
Credits	02					-	17	Yes
Cumulative Sum	05	24	24	56	15	15	05	

PROGRESSIVE TOTAL CREDITS: 144+19 =163

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and Technology, Nagpur

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Program Elective- I	Program Elective- II	Program Elective- III
Semester V	Semester V	Semester VI
BEC3506 Digital System Design	BEC3509 Introduction to MEMS	BEC3603 Antenna and Microwave Engineering
BEC3507 Embedded Systems	<b>BEC3510</b> Information Theory and Coding	BEC3604 Optical Communication
BEC3508 Power Electronics	BEC3511 Biomedical Instrumentation	BEC3605 Mechatronics
Program Elective-IV	Program Elective-V	
Semester VI	Semester VII	
BEC3606 PLC SCADA	BEC4703 Robotics & Automation	
BEC3607 Wireless & Sensor Network	BEC4704 Machine learning	
BEC3608 Speech Processing	BEC4705 Satellite Communication	

		Lis	t of (	Open Elective	
Sr. No.	<b>Course Code</b>	Course Title	Sr. No.	Course Code	Course Title
1	BCSXX01	Cyber Law and Ethics	9	BMEXX09	Nanotechnology and Surface Engineering
2	BCSXX02	Block chain Technology	10	BMEXX10	Automobile Engineering
3	BITXX03	Cyber Security	11	BEEXX11	Power Plant System
4	BITXX04	Artificial Intelligence	12	BEEXX12	Electrical Materials
5	BECXX05	Internet of Things	13	BAEXX13	Avionics
6	BECXX06	Embedded Systems	14	BAEXX14	Unmanned Aerial Vehicles
7	BCEXX07	Introduction to Art and Aesthetics	15	BBTXX15	Biomaterials
8	BCEXX08	Metro Systems and Engineering	16	BBTXX16	Food and Nutrition Technology

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		BEC4	701: Computer Communication	n Network				
Т	<b>Ceachi</b> r	ng Scheme		Examinati	ion Scheme			
Lectu	ires	3 Hrs/week		CT-1	15 Marks			
Tutor	ial	-		CT-2	15 Marks			
Total Cro	<b>Total Credit</b> 3			CA	10 Marks			
				ESE	60 Marks			
				Total	100 Marks			
				Duration of ESE:03 Hrs 00 Min.				
			<b>Course Contents</b>					
Unit I	half d	luplex, full duple	rks, Network Topology, Network Devic k, Network Classification:- LAN,MAN,V TCP/IP Reference Model. Transmission	VAN, Network Arch	itecture, Protocols,			
Unit II	Design Issues, Framing methods, Flow Control and Error Control, Stop-and-wait flow control, Sliding-window flow control, Stop-and-wait ARQ, Go-back-N ARQ, Selective- repeat ARQ, HDLC, MAC sub layer: ALOHA							
Unit III	and S	Subnet, Routing	Routers, IP addressing and its classification algorithms like Shortest path routing, ector Routing, Dynamic Routing. Routing	Djkstra's algorithi				
Unit IV	mode	el, TCP:- TCP hea	ces, Connection oriented & Connection der format, comparison between UDP are an, Quality of Service (QoS), Token bucket	nd TCP, Need of Cor	ngestion control,			
Unit V	Intro	duction to Cryp	NS, Electronic Mail, File Transfer (Fitography, Secret key algorithm, publicurity:DMZ,NAT	* *				
Text Boo	ks							
T.1	Comp	uter Networks: A	ndrew Tanenbaum, 4th Edition, PHI.					
T.2	Comp	uter Communicat	ion Networks : Frouzan, 4th Edition, Tata	Mc-Graw Hill				
Т.3	Willia	m Stallings, "con	puter Networks and Cryptography", 3rd	edition, Pearson Edu	cation			
Reference	e Book	XS .						
R.1	Teleco	ommunication Sw	itching systems & Networks: Vishwanath	nan , 3 <sup>rd</sup> Edition,PHI				
R.2		uter Communicat						

R.3	Com	munication Networks: Leon-Gracia						
Useful L	inks							
1	htt	tps://nptel.ac.in/courses/106/105/106105080/						
2	htt	nttps://nptel.ac.in/courses/117/105/117105076/						
3	htt	http://nptel.ac.in/courses/117103064						
Course (	Code	Course Outcomes	CL	Class Sessions				
BEC470	1.1	<b>Explain</b> the fundamentals of Computer Network and Network topologies.	4	9				
BEC470	1.2	Interpret Flow control & Error control protocols of Data Link Layer with ARQ.	2	9				
BEC470	1.3	Illustrate the concept of IP Addressing techniques and Routing protocols of Network Layer.	4	9				
BEC470	1.4	Analyze the transport layer services, protocol Headers and congestion control protocols.	4	9				
BEC4701.5		<b>Determine</b> the function of Application Layer and Presentation layer protocols.	3	9				

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	eacming	g Scheme				Examinatio	on Scheme	
Lectu	res	3 Hrs/week				CT-1	15 Marks	
Tutori	al	-				CT-2	15 Marks	
Total Cre	edit	3				CA	10 Marks	
						ESE	60 Marks	
						Total	100 Mark	
					D	uration of ESE:	:03 Hrs 00 Min.	
			Cou	rse Contents	<b>,</b>			
	MOS	TRANSISTO	ORS					
Unit I	nMOS	S enhancemen	t and pMOS enl	hancement tra	nsistor, thre	shold voltage,	body	
	effect	, MOS effect,	MOSdevice equa	tions, small s	ignal model	for MOS transi	istor.	
	СМО	S INVERTE	3					
Unit II	Princi	ple of operation	on, de characteris	stics, transien	t characterist	ics, $\beta_n/\beta_p$	ration, noise	
			MOS inverter, tr				OS inverter.	
	STUI	OY OF CMOS	SLOGIC					
Unit III	Study	Study of combinational logic, gates, compound gates, multiplexers, and memory element						
		of combinatio	nai logic, gates,	compound g	ates, multipl	exers, and me	mory elements	
	using	CMOStechno	logy.					
	using	CMOStechno	0 0					
Unit IV	using CIRC	CMOStechno CUIT CHARA	logy.	N AND PERI	FORMANC	E ESTIMATI	ON	
Unit IV	using CIRC Resist sharin	CMOStechno CUIT CHARA cance and capac g.	logy. CTERIZATIO	N AND PERI	FORMANC	E ESTIMATI	ON	
Unit IV	using CIRC Resist sharin	CMOStechno CUIT CHARA cance and capac	logy. CTERIZATIO	N AND PERI	FORMANC	E ESTIMATI	ON	
	using CIRC Resist sharin VLSI	CMOStechno CUIT CHARA cance and capac g. DESIGN	logy. CTERIZATIO	N AND PERI	FORMANC	E ESTIMATION S., power dissipa	ON ation, charge	
Unit IV Unit V	using CIRC Resist sharin VLSI VLSI latch	CMOStechno CUIT CHARA cance and capacing. DESIGN processing intup, CMOS cir	citance estimation egration, layout or cuits and logic	N AND PERI n, switching cl design rules, a design: trans	FORMANC haracteristics and stick diagistor sizing,	E ESTIMATION OF THE PROPERTY O	ON ation, charge ation t and	
	using CIRC Resist sharin VLSI latch physic	CMOStechno CUIT CHARA cance and capacing. DESIGN processing intup, CMOS circular design of	citance estimation	N AND PERI n, switching cl design rules, a design: trans	FORMANC haracteristics and stick diagistor sizing,	E ESTIMATION OF THE PROPERTY O	ON ation, charge ation t and	
Unit V	using CIRC Resists sharin VLSI VLSI latch physics strates	CMOStechno CUIT CHARA cance and capacing. DESIGN processing intup, CMOS circular design of	citance estimation egration, layout or cuits and logic	N AND PERI n, switching cl design rules, a design: trans	FORMANC haracteristics and stick diagistor sizing,	E ESTIMATION OF THE PROPERTY O	ON ation, charge ation t and	
Unit V  Text Book	using CIRC Resist sharin VLSI latch physic strateg	CMOStechno CUIT CHARA cance and capacing. DESIGN processing intup, CMOS circular design of gies.	egration, layout or cuits and logic simple logic ga	n, switching cludesign rules, a design: translates, CMOS	haracteristics and stick diagistor sizing, logic struct	E ESTIMATION OF THE PROPERTY O	ON ation, charge ation t and king	
Unit V	using CIRC Resist sharin VLSI latch physic strateg	CMOStechno CUIT CHARA cance and capac g. DESIGN processing int up, CMOS circal design of gies.	citance estimation egration, layout or cuits and logic	n, switching cludesign rules, a design: translates, CMOS	haracteristics and stick diagistor sizing, logic struct	E ESTIMATION OF THE PROPERTY O	ON ation, charge ation t and king	
Unit V  Text Bool T.1	using CIRC Resist sharin VLSI latch physic strateg ks Princip Series.	cmoStechnocular CHARA cance and capacing. DESIGN processing intup, CMOS circular design of gies. Dal of CMOS V	egration, layout or reuits and logic simple logic ga	design rules, a design: translates, CMOS	conductive that the struct of	E ESTIMATION OF THE PROPERTY O	ON ation, charge ation t and king	
Unit V  Text Book	using CIRC Resists sharin VLSI latch physics strates ks Princip Series. "Digital	CMOStechno CUIT CHARA cance and capacing. DESIGN processing intup, CMOS circular design of gies. Dal of CMOS V	egration, layout or cuits and logic simple logic ga	design rules, a design: translates, CMOS	conductive that the struct of	E ESTIMATION OF THE PROPERTY O	ON ation, charge ation t and king	
Unit V  Text Bool T.1	using CIRO Resists sharin VLSI latch physics strates Princip Series. "Digitate Chande	CMOStechno CUIT CHARA cance and capacing.  DESIGN  processing intup, CMOS circular design of gies.  Dal of CMOS Vall Interrogated rakasan, and E	egration, layout or reuits and logic simple logic ga	design rules, a design: translates, CMOS	c, K. Eshragh	gram representation fan-in, fan-outures and clock	ON ation, charge ation t and king	

R.1	"VLSI Technology", S.M. Sze, McGraw Hill Publications						
R.2	"VLSI Design Technologies for Analog & Digital Circuits", Randall L Gei, McGraw Hill Publications						
R.3	Communication Networks: Leon-Gracia						
Useful L	inks						
1	https://nptel.ac.in/courses/106/105/106105080/						
2	https://nptel.ac.in/courses/117/105/117105076/						
3	http://nptel.ac.in/courses/117103064						

Course Code	Course Outcomes	CL	Class Sessions
BEC4702.1	<b>Explain</b> the fundamentals of Computer Network and Network topologies.	4	9
BEC4702.2	<b>Interpret</b> Flow control & Error control protocols of Data Link Layer with ARQ.	2	9
BEC4702.3	<b>Illustrate</b> the concept of IP Addressing techniques and Routing protocols of Network Layer.	4	9
BEC4702.4	<b>Summarize</b> the transport layer services, protocol Headers and analyze the congestion control protocols.	2	9
BEC4702.5	<b>Determine</b> the function of Application Layer and Presentation layer protocols.	3	9

BOS Chairman

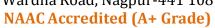
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		<u> </u>		otics and Auto	omation	8 8		
Т	'eachir	ng Scheme				nation Scheme		
Lectu	res	3 Hrs/week			CT-1	1		
Tutor	ial	-			CT-2	2 15 Marks		
Total Cre	edit	3			CA	10 Marks		
					ESE	60 Marks		
					Total	100 Marks		
				Duration of ESE: 03 Hrs 00				
			Cou	rse Contents	1			
Unit I	open-			-		bots, closed loop and manipulators, Social		
Unit II	Robot Kinematics and Dynamics: Kinematic Modelling: Translation and Rotation Representation, Coordinate transformation, DH parameters, Jacobian, Singularity, and Statics Dynamic Modelling: Equations of motion: Euler-Lagrange formulation							
Unit III	Introd	duction to	Cameras,	Camera calib	y, Position, Velocit ration, Geome ations, Vision applie			
Unit IV	advar	nced controls, I	Robot Actuation S	Systems: Actuators		PID Non-linear and ulic and Pneumatic; etuators.		
Unit V			-	pedded systems: A Robot Application		egration with sensors,		
Text Boo	ks							
T.1	Saha, S	S.K., "Introduction	n to Robotics, 2nd	Edition, McGraw-H	Hill Higher Education	on, New Delhi, 2014.		
T.2	Mittal	R.K. and Nagratl	ı I.J., "Robotics and	d Control", Tata Mo	cGraw Hill.			
T.3	Mukhe	erjee S., "Robotic	s and Automation"	, Khanna Publishing	g House, Delhi.			
Referenc	e Book	ΚS						
R.1	Craig,	J.J., "Introductio	n to Robotics: Mec	hanics and Control'	', Pearson, New Del	lhi, 2009		
R.2	Steve l	Heath, "Embedde	d System Design",	2 nd Edition, Newr	nes, Burlington, 200	3		
R.3	Ghosa	al, A., "Robotics",	Oxford, New Dell	ni, 2006				
Useful Li	nks							

1	https://nptel.ac.in/courses/112/101/112101098/
2	https://nptel.ac.in/courses/112/105/112105249/

<b>Course Code</b>	Course Outcomes	CL	Class Sessions	
BEC4703.1	703.1 Compute the currents and voltages of each element in electrical network by using loop (mesh) as well as node analysis.			
BEC4703.2	<b>Determine</b> current, voltage & power of electrical circuit's with the help of various theorems for the analysis.			
BEC4703.3	Attribute the Solution of First and Second order networks and transient and steady-state response of electrical circuits	4	9	
BEC4703.4	BEC4703.4 Discriminate the integro-differential equation using Laplace and inverse Laplace transformation to analyze the behavior of electric circuit  4		9	
BEC4703.5	<b>Solve</b> the two port network in respect of the various approaches developed to analyze their behavior.	3	9	

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	BEC4704: Machine Learning					
T	'eachi	ing Scheme			Examinat	ion Scheme
Lectu	res	3 Hrs/week			CT-1	15 Marks
Tutor	ial	-			CT-2	15 Marks
Total Cro	edit	3			CA	10 Marks
					ESE	60 Marks
					Total	100 Marks
					Duration of ES	E:03 Hrs 00 Min.
				urse Contents		
Unit I	dens		•	abilities of events, rables, estimation of		
Unit II	Reg	<b>Overview of Machine learning concepts: Introduction</b> to Bayes Theorem, Linear Regression- model assumptions, regularization. Over fitting and train/test splits, Types of Machine learning - Supervised, Unsupervised, Reinforced learning.				
Unit III	<b>Classification and Regression Algorithms</b> : Naïve Bayes, K-Nearest Neighbors, logistic regression, support vector machines (SVM), decision trees, and random forest and their classification Errors.					
Unit IV	<b>Clustering :</b> Sequential clustering, hierarchical clustering, probabilistic clustering, partitional clustering, clustering for region segmentation, Introduction to Neural Networks, backpropagation algorithm, Overview of Deep Learning.					
Unit V		Case Studies of Machine Learning Application: Weather forecasting, Stock market prediction, Object Detection and recognition, Real Time Applications.				
Text Boo	ks					
T.1		Aurélien Géron, "Hands-On Machine Learning with Scikit - Learn and Tensor Flow: Concepts, Tools, and Techniques to Build Intelligent Systems", 1st Edition, O'Reilly Media.				
T.2	Jeeva Jose, "Machine Learning", Khanna Publishing House, Delhi.					
T.3	Learning by Subramanian Chandramouli, Saikat Dutt, Amit Kumar Das.					
Referenc	e Boo	ks				
R.1		ood fellow, Yos //www.deeplearn	-	Aaron Courville, "D	eep Learning", MIT	Press
R.2	Chop	ora Rajiv, "Mach	ne Learning", I	Khanna Publishing H	ouse, Delhi.	

R.3	Joel Grus, "Data Science from Scratch: First Principles with Python", O'Reilly Media.			
Useful Li	inks			
1	https://nptel.ac.in/courses/106106139			
2	https://www.youtube.com/watch?v=fC7V8QsPBec			
3	https://www.digimat.in/nptel/courses/video/106105152/L01.html			

Course Code	Course Outcomes	CL	Class Sessions	
BEC4704.1	Understand the scope and examples of machine learning.	2	9	
BEC4704.2	Explain fundamentals of machine learning principles and algorithm.			
BEC4704.3 Prepare clean data for machine learning.		4	9	
BEC4704.4	Analysis supervised and unsupervised learning models to solve problems.			
BEC4704.5 Prepare clean data for machine learning.		3	9	

BOS Chairman

Department of Electronics & Comm

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		]	BEC4705 : Satellite Communicat	ion	
7	<b>Feachi</b>	ng Scheme		Examinat	ion Scheme
Lectu	Lectures 3 I			CT-1	15 Marks
Tutor	ial	-		CT-2	15 Marks
Total Cr	edit	3		CA	10 Marks
				ESE	60 Marks
				Total	100 Marks
				Duration of ES	E: 03 Hrs 00 Min.
			Course Contents		
Unit I	sate in t dete	llite communication in the contraction in the cont	ORBITAL ASPECTS OF SATELLITE COM ons, Orbital mechanics, Keplers laws of pl g the Satellite with respect to the e perturbations, launches and launch vehice	lanetary motion, Lo earth, Orbital elei	ocating the satellite ments, Look angle
Unit II			MS: Introduction, attitude and orbit coring, Power Systems, Communication Sub	•	• •
Unit III	III SATELLITE LINK DESIGN: Introduction, Basic transmission theory, System noise temperature and G / T ratio. Design of uplink and down link models, Design of satellite links for specified C / N ratio.  EARTH STATIONS: Introduction, Transmitters, Receivers, Antennas, Tracking systems, Terrestrial interface, Primary power, test methods.				d C / N ratio.
Unit IV	LOW EARTH ORBIT AND NON-GEO STATIONARY SATELLITE SYSTEMS : Introduction, Orbit				· ·
Unit V	SATELLITE NAVIGATION & THE GLOBAL POSITIONING SYSTEM: Introduction, Radio and Satellite Navigation, GPS Position Location principles, GPS Receivers and codes, Satellite signal acquisition, GPS Navigation Message, GPS signal levels, GPS receiver operation, GPS C/A code accuracy, Differential GPS				
Text Boo	ks				
T.1	T Pra	tt and W Bostiain -	Satellite Communications, 2nd Edition, Jo	hn Wiley, 2003.	
T.2		Wilbur L. Pritchard, Henri G.Suyderhoud and Robert A Nelson - Satellite Communication Systems Engineering, 2nd Edition, Pearson Publications, 2003.			
T.3	Satellite Communications SystemsSystems, Techniques and Technology By <u>Gerard Maral</u> , <u>Michel Bousquet</u> , <u>Zhili Sun</u> · 2020				
Referenc	e Boo	ks			
R.1	Denn	is Roddy, Satellite	communications, McGraw Hill, 4 th Edition	on,2009.	
R.2	. DC Agarwal, Satellite Communications, Khanna Publishers, 2003 Robert M Gagliard, Satellite				

	Communications			
R.3	Satellite Communications Systems Systems, Techniques and Technology			
13.5	By Gerard Maral, Michel Bousquet, Zhili Sun · 2020			
Useful L	Useful Links			
1	http://nptel.iitm.ac.in/courses/			
2	2 https://archive.nptel.ac.in/courses/117/105/117105131/			
https://encryptedtbn2.gstatic.com/faviconV2?url=https://www.digimat.in&client=ABOUT_THIS_RESULT&size=32&type=FAVICON&fallback_opts=TYPE,SIZE,URL				

Course Code	Course Outcomes	CL	Class Sessions
BEC4705.1	<b>Determine</b> orbital mechanics, Kepler's laws, locating, elements, launches, and communication effects in satellite systems.	2	9
BEC4705.2	<b>Examine</b> satellite subsystems and multiple access techniques for comprehensive understanding and application	3	9
BEC4705.3	<b>Analyze</b> satellite link & earth station design principles for effective communication.	4	9
BEC4705.4	<b>Examine</b> essential concepts of Slow Earth Orbit & Non-Geo Satellite Systems, covering orbits, coverage, delays, throughput, system designs.		
BEC4705.5	<b>Examine</b> proficiency in Satellite Navigation & GPS principles, covering signals, receivers, accuracy, and Differential GPS.	3	9

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	BEC4	706: Computer Comr	nunication Network	Lab	
T	eaching Scheme		Examina	tion Scheme	
P	ractical	2 Hrs/week	CA 25 Marks		
Tot	al Credit	1	ESE	25 Marks	
	<u> </u>		TOTAL	50 Marks	
			Duration of ES	SE: 02 Hrs 00 Mi	n.
Sr. No.		List of Expo	eriment		СО
1	Study network sin	mulator & get familiar with NS	32		CO1
2	create network To	opology in NS2.			CO 1
3	demonstrate data	transmission using Ping protoc	col, tracert, IP configuration	& hub.	CO2
4	Study the fundam	nental of socket programming.			CO2
5	Interpret IP address of the system, dhcp, network addresses translation.			CO2	
6	To understand the domain name server.			CO3	
7	To Study Protocol analyzer.				CO3
8	To Study of FTP,	HTFT protocol.			CO4
9	To perform PC to	PC communication using RS-	232 port.		CO4
10	To demonstrate N	etwork security cryptography			CO5
Text Boo	ks				
T.1	Wireless Comm	unications, Principles, Practic	e – Theodore, S. Rappaport,	PHI, 2nd Edn.	
T.2	Wireless Comm	unication and Networking – W	Villiam Stallings		
Reference					
R.1	Wireless Digital	Communications – KamiloFe	her		
R.2	Principles of Wi	ireless Networks – KavehPahL	aven and P. Krishna Murth	у	
Useful Li	nks				
1	https://nptel.ac.in	n/courses/103/106/103106118/			
2	https://nptel.ac.ir	n/courses/117/105/117105147/	-		

Course Code	Course Outcomes	CL	Lab Sessions
BEC3610.1	Interpret OSI/TCP reference model for data transfer in a network	2	2
BEC3610.2	<b>Determine</b> principle of computer communication including multiplexing, flow control and error control.	3	2
BEC3610.3	Analyze wide area, local area and metropolitan area network.	4	2
BEC3610.4	Understand IEEE standards such as 802.11, 802.16, IP addressing, etc	2	2
BEC3610.5	<b>Implement</b> security of a network using cryptography technique such as public key, secret key.	3	2

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Progra	m: R. Tech Elect	tronics & Communication Engineer	ring					
Semeste	1	Name of Course		Т	P	(	Credits	
VI	BEC4707	CMOS VLSI DESIGN	-	-	2		1	
Sr. No.		List of Experiment	<b>"</b>	l			CO	
1	Design of CMOS I	nverter using DSCH2 Tool.					CO1	
2	Gate Level Analys	ate Level Analysis of 2-Input NAND & NOR Gate.						
3	Implement the Foll	implement the Following Function using Compound Gates. F(A,B,C,D)=(ABC+CD)' CO2						
4	Design Half Adder	esign Half Adder using NAND Gates.						
5	Design Full Adder	using NAND Gates.					CO3	
6	Design 2:1 Multipl	exer using NAND Gates.					CO3	
7	Design of 4 bit bin	ary Adder					CO4	
8	Draw Layout of CM	MOS Inverter Microwind/Cadence Tools					CO4	
9	Draw Layout of 2-1	input NAND Gate using Microwind /Cader	nce Tools				CO5	
10	Draw Layout of M	ultiplexer					CO5	
Text Bo	oks							
T.1	Principle of CM	IOS VLSI Design by Neil H. E. WesteHarr	ris4th Editi	on, 20	)13			
T.2	Introduction to VLSI Circuits and Systems by John P. UyemuraFirst Edition							
Referen	ce Books							
R.1	CMOS VLSI D	esign by Pucknell, K. Eshraghian3rd Edition	on, 2005					
R.2	CMOS Digital leblebiciThird	Integrated circuits Analysis and Design by edition, 2008	Sung-Mo	Kang,	Yusu	f		
Useful I	inks							
1	nptel.ac.in/noc2	1_ee09/preview						
2	nptel.ac.in/courses/108/107/108107129/							
		Course Outcomes						
EC2308	.1 Describe and in	terpret the basic concepts of MOS transisto	ors					
EC2308	Construct the ability to design a system, component or process as per needs and specifications.							

EC2308.3	Analyze inverter design, characteristics and applications and performance parameters of CMOS Circuits.
EC2308.4	Evaluate circuits using CMOS styles
EC2308.5	Analyze performance of the complex logic structures

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