



**TULSIRAMJIGAIKWAD-PATIL College of Engineering and Technology**  
Wardha Road, Nagpur-441108 Accredited with NAAC A+ Grade  
Approved by AICTE, New Delhi, Govt. of Maharashtra  
(An Autonomous Institution Affiliated to RTM Nagpur University)



## Department of Electronics & Communication Engineering

B.Tech Electronics & Communication Engineering

Course Name: **BEC3506**: Digital System Design

<b>EC3506</b>	<b>Course Outcome</b>
CO1	Analyze 2- bit adder/ subtractor using XOR and NAND gates.
CO2	Construct 4:1 multiplexers using logic gates and Implement full adder using 4:1 Multiplexers.
CO3	Design and simulate of basic logic gates using HDL.
CO4	Simulate the combinational circuits such as adders, subtractors, multiplexers and decoders using HDL.
CO5	Simulate the sequential circuits such as counters and flip-flops using HDL.